1. Software stack

HARDWARE

KERNEL MODE

USER MODE

Application

Driver(scala)

PCIE interface

PCIE-DMA driver(c)

FPGA

1. Mandatory interface signals

Input clk

Input reset

Input we

Input [32:0] readaddr

Input [32:0] writeaddr

Input [64:0] writedata

Output [64:0] readdata

1. Mandatory registers

|  |  |
| --- | --- |
| Service id (0,63) | |
| Status (64,127) | |
| Base address (128,191) | Buffer size (192,255) |
|  | |